

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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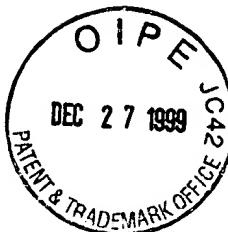
TECH CENTER 2700

In re Application of:

Massino Sutera, Alan Smith

Serial No. 09/430,350

Filed: October 29, 1999

For: METHOD FOR REDUCING NOISE IN
AN INTEGRATED CIRCUIT LAYOUT

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Examiner:

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CERTIFICATE OF MAILING
I hereby certify that this correspondence is
being deposited with the United States Postal
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Assistant Commissioner for Patents
Washington, D.C. 20231
Date: 12/20/99
By: Sean P. Lewis

TRANSMITTAL LETTER

Honorable Assistant Commissioner
for Patents
Washington, D.C. 20231

Sir:

Enclosed for filing please find the Power of Attorney for the above
described case.

Respectfully submitted,

Sean P. Lewis
Reg. No. 42,798

Dated: December 20, 1999